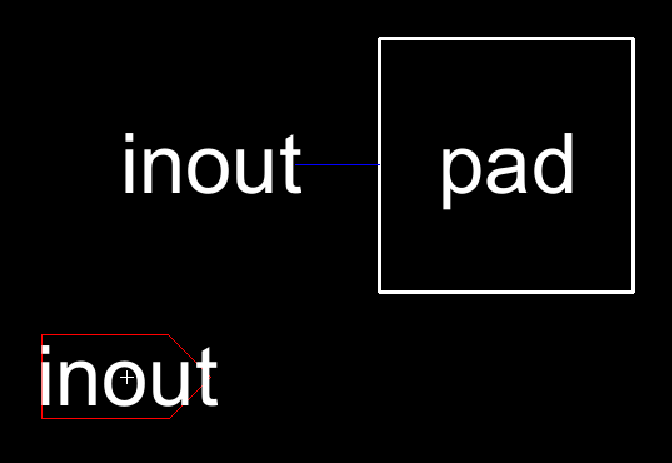
# VLSI Lab 2 – Padframe

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## Schematic

### Pad cell



### DAC

A screenshot of a computer

AI-generated content may be incorrect.

Padframe was made with 8 pins since it’s a 2x2 package but DAC only needs 7 pins so the extra pin is connected to ground.

### Padframe

A computer screen shot of a computer

AI-generated content may be incorrect.

## Layout

### A computer screen shot of a computer chip AI-generated content may be incorrect.

Routing was done entirely on metal 2 to be able to go over the wiring within the DAC itself.